## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**CETIN KAYA** 

09/620,649

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Serial No. 09/620,643 (TI-23686.1)

FEB 0 5 200/

Filed July 20, 2000

For: INTEGRATED CIRCUIT HAVING INTEGRATED CIRCUIT HAVING INTEGRATED ARRAY AND PERIPHERAL ISOLATION DIELECTRICS

Art Unit 2822

Examiner Mary A. Wilczewski

Customer No. 23494

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## CERTIFICATE OF MAILING OR TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the attached document is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to Director of the United States Patent and Trademark Office, P.O. Box 1450., Alexandria, VA 22313-1450 or is being facsimile transmitted on the date indicated below:

Jay M. Cantor, Reg. No. 19,906

## **DECLARATION UNDER 37 C.F.R. 1.131**

Sir:

## CETIN KAYA declares as follows:

- 1. That he is the applicant in the subject application.
- 2. That he conceived the subject invention prior to January 16, 1998 as evidenced by provisional application Serial No. 60/060,561, filed September 30, 1997 which sets forth his invention and which he assisted in preparation and approved for filing, a copy of which is attached hereto.
- 3. Diligence up to the filing of the parent of the subject application is proved by the fact that the parent of the subject application, patent application Serial No.

TI- 23686.1-1

09/168,047, was filed October 7, 1998, there being only 6 days hiatus from the expiration of the provisional application and the regular application which is the parent of the subject application.

I declare under penalty of perjury that the foregoing is true and correct.

10/10/2006

(date)

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